



# GTS TCAD Framework

*A Suite of Tools Providing the Answers You Need*

**Optimization • Reliability & Variability • Path-Finding • DTCO**



*Working Environment, Project Management, DOE*

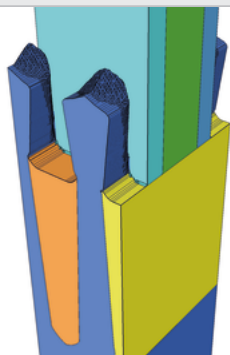
Setup	Details	Scripting	Script_out	
010 ProEmu	009 Minimos	app001 appr	011 Vision	Output
Islepid	Length	Width	IsOr	IsCir
1.81e-8	1.0e-8	100	110	0.0
2.81e-8	1.0e-8	100	110	0.1
3.81e-8	1.0e-8	100	110	0.2
4.81e-8	1.0e-8	100	110	0.3
Gate	Drain	VDD	over	
0.05	0.05	0.9	[TF009 Device Gate]	
0.05	0.05	0.9	[TF009 Device Gate]	
0.05	0.05	0.9	[TF009 Device Gate]	
Drain	Ion	Vth_in	Tasks	Files
3.955e-10	8.558e-12	2.345e-03	01_02_03_04	192_168_16...
			01_05_03_04	009 Minimos finished(0)
			01_06_03_04	192_168_16...
			01_07_03_04	009 Minimos running
				192_168_16...
				009 Minimos



## Process Simulation

PROEMU

**Advanced topology simulation (level-set)**  
**Anisotropic epi-growth through surface diffusion models**  
**Dopant implantation and diffusion model**  
**Realistic geometries**  
**Inclusion of parasitics**  
**Physical process models**



## Device and Circuit Simulation

MINIMOS-NT

**General-purpose semiconductor device simulator**  
**Reliability (BTI, HCD, SET, SEU, SER, etc.)**  
**Variability (RDD, MGG, LER, etc)**  
**NVM (FG, SONOS, 3D-NAND)**  
**FinFET, Nanosheet, CFET**  
**DTCO**

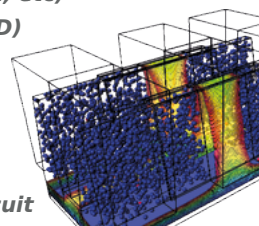


MIXED MODE



MATERIALS

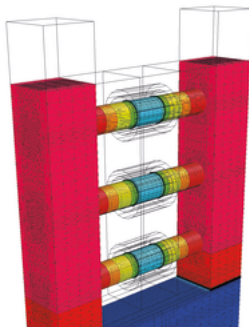
**Mixed Mode: Device in circuit**



## Structure Generation / Editing

STRUCTURE

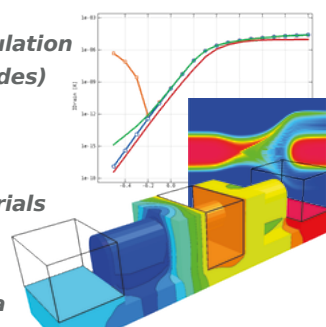
**Template-based structure generation**  
**3D device CAD for geometry and doping**  
**Unstructured meshes for modeling true geometry**  
**Automatic refinement**  
**Customizable templates**  
**Import of 3<sup>rd</sup>-party files**



## Nano Device Simulator

VSP

**Physical device simulation**  
**FinFET (14/10nm nodes)**  
**Stacked nanosheets (3/2/1.5 nm nodes)**  
**Tunneling FETs**  
**Novel channel materials**  
**Stress, strain**  
**Crystal orientation**  
**Import ab-initio data**



## Compatible

3RD PARTY

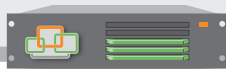
**Integration of 3<sup>rd</sup>-party software**  
**Import / export of industry-standard files with native grid (full accuracy)**



## Fully Customizable

SDK

**SDK for easy implementation of custom models with full access to internal models and solvers**  
**Tools integrable in 3<sup>rd</sup>-party workbenches**

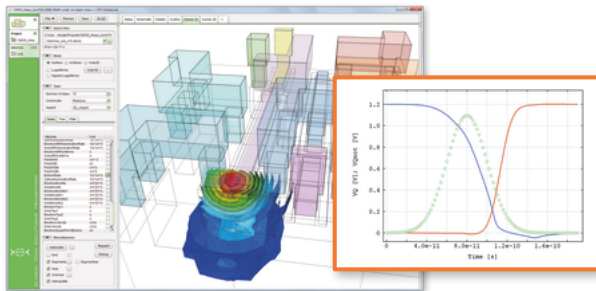


## Powerful Computational Infrastructure

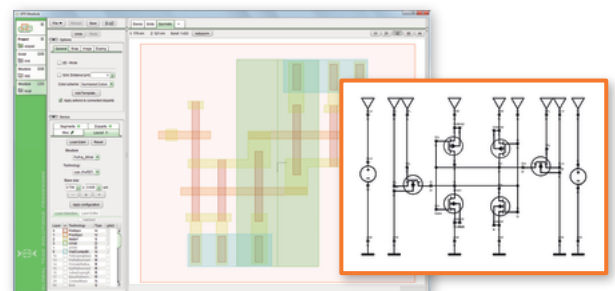
GTS JOB SERVER

**Automatic optimization of resource assignment in grid and cloud environments**

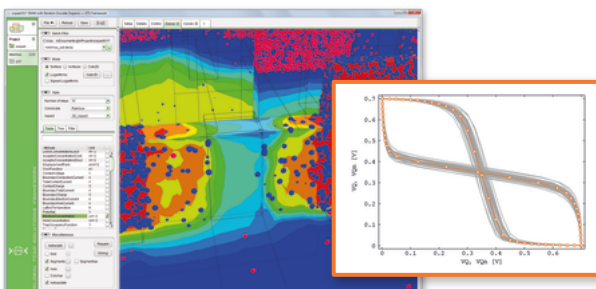
# Profound TCAD, Based on Research and Physics



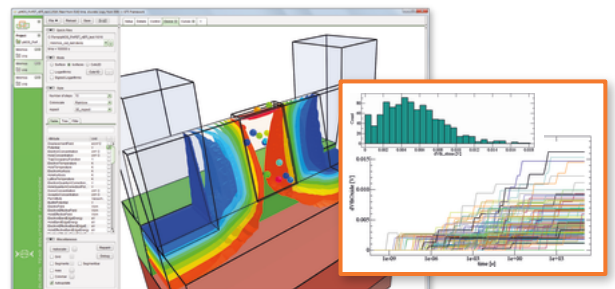
**2D/3D Device & Process Simulation**  
Planar CMOS, FD-SOI, FinFET, Nanosheet transistors



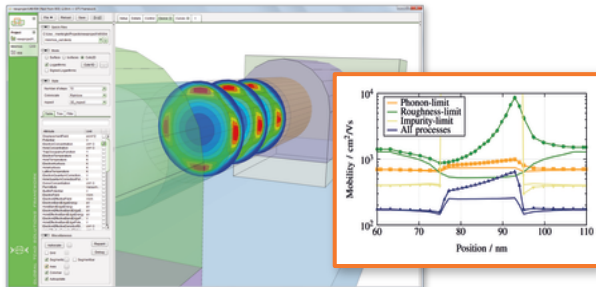
**Circuit / Mixed Mode Simulation**  
Compact model extraction, SRAM, full-cell analysis



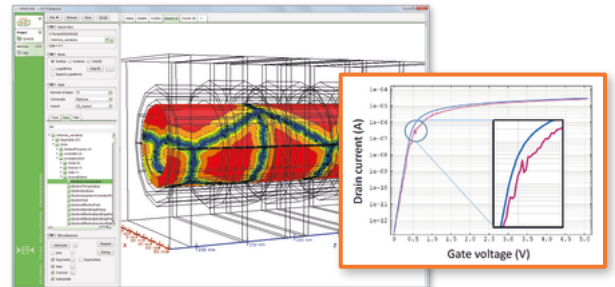
**Variability (RDD, MGG, LER, GER, Process)**  
Statistical device & circuit analysis and optimization



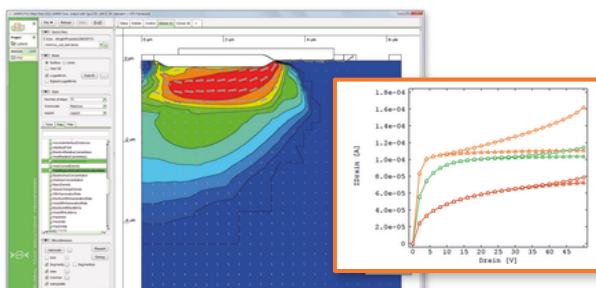
**Reliability (BTI, HCD, TDD, RTN)**  
Radiation effects (SET, SEU, TID)



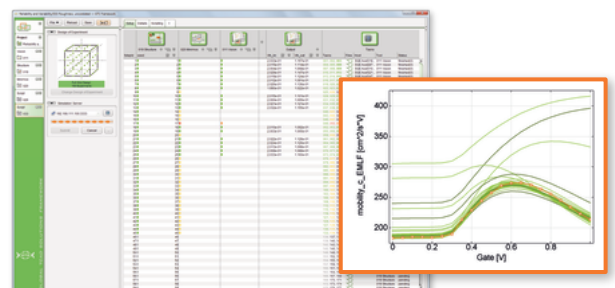
**Emerging Devices / Path Finding**  
Nano-wire transistors, novel materials (SiGe, III/V, GeSn), TFETs; **Physical device simulation** (SB-BTE, BTBT)



**Non-Volatile Memory**  
NOR Flash, NAND Flash, SONOS, 3D-NAND  
Program / erase, endurance, data retention, variability



**Analog Devices, RF Applications, TFTs, Power Devices, Wide band-gap (SiC, GaN)**



**DOE, Optimizer, Scripting, Distributed Computing (Grid & Cloud)**